438 106

09/12/00

438 Class
106 Subclass
ISSUE CLASSIFICATION

PATENT NUMBER

C

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED Q.A.	

APPLICATION NO.	CONT/PRIOR	CLASS	SUBCLASS	ART UNIT	EXAMINER Toledo
09/660324	D	257 438	106	2815	

APPLICANTS

Kie Ahn
Leonard Forbes

TITLE

Silicon multi-chip module packaging with integrated passive
components and method of making

PTO-2040
12/99

ISSUING CLASSIFICATION

ORIGINAL			CROSS REFERENCE(S)				
CLASS	SUBCLASS	CLASS	SUBCLASS (ONE SUBCLASS PER BLOCK)				
438	106	438	110	384			
INTERNATIONAL CLASSIFICATION		257	728				
H 0 1 L	21/44						
H 0 1 L	21/48						
H 0 1 L	21/50						
				☐ Continued on Issue Slip Inside File Jacket			

TERMINAL DISCLAIMER ☐	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
	7	12	4	31	1
☐ The term of this patent subsequent to _____ (date) has been disclaimed.	_Fernando Toledo_ 7/25/03 (Assistant Examiner) (Date)			NOTICE OF ALLOWANCE MAILED	
☐ The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____				ISSUE FEE	
				Amount Due	Date Paid
	(Primary Examiner) (Date)			ISSUE BATCH NUMBER	
☐ The terminal ____months of this patent have been disclaimed.	(Legal Instruments Examiner) (Date)				

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